



RF ASIC designer

Responsible for design of high-frequency ASICs for integration in optical receiver (ROSA) and transmitter (TOSA) modules

Workplace:

Lyngby (Denmark)

Company:

Bifrost Communications Aps is a modern start-up enterprise spun out from Technical University of Denmark (DTU) north of Copenhagen. With its Quasi-Coherent Transceiver Technology, Bifrost facilitates the need for higher data rates in 5G and 6G DWDM Fronthaul networks, Metro ring networks, and ultra-high sensitivity burst-mode OLT receivers for TWDM-Pon links

In a plug and play cost effective solution customers can upgrade the DWDM links from 10G to 25G with reach up to 40 km in Fronthaul applications and up to 60 km in Metro-ring networks. In addition, we are working currently developing 50 Gbps and 100 Gbps solutions based on a higher level of photonics- and electronic integration and co-packaging.

Bifrost has gained significant customer traction on its first generation receivers and have obtained Grant and Funding from the EIC accelerator and other investors to substantially pursue upscaling of development and production of its next generation products. The company is managed by experienced entrepreneurs with successful track records from similar start ups in the optical and opto-electronic space.

Job description:

You will be part of a team that has developed and patented a Optical Receiver which increases reach and capacity in fiberoptic networks. The high-speed radio-frequency ASIC it a key part pf the solution. The receiver is well renowned for its unique capabilities at data rates of 25 Gbps, and now the challenges are scale-up from prototype as well as the development of 50 Gbps and 100 Gbps solutions.

As an experienced RF ASIC designer with deep knowledge and understanding of signal integrity above 100 GHz, you will play in important role in these developments. Your tasks will include:

- 1) Development of high-speed (>100 Gbps) analog signal processing ASICs and transimpedance amplifiers
- 2) Signal integrity analysis and 3D RF design of pachaging, flex-PCB transmission line PCBs etc.
- 3) Integration of the RF ASIC into the optical package

4) Tests and analysis of test results

5) Responsibility for various documentation material, BOM, drawings, contact to chip foundries, revision control and tracking of design changes.

5) You will work closely together with your colleagues internally in the organization as well as externally with customers and partners.

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| Personal characteristics: | You are a highly self-motivated person who takes initiative by yourself. You work in a structured manner and is capable of working highly independent. |
| Education: | You have an educational background as M.Sc eller Ph.d. in engineering, physics or other relevant field. |
| Work experience: | You will be working closely alongside a senior RF designer, so although experience is a plus, we encourage you to apply even if you have recently graduated. |
| Language & IT skills: | You are fluent in English (spoken and written), and you are experienced user of Cadence, Keysight ADS, or similar tools. |
| Salary: | You will receive a good remuneration corresponding to your qualifications, and you will have the opportunity to be part of a warrants program. |
| You are offered: | You will be offered an exciting position with good development potential both professionally and personally. In addition, you will get a chance to become part of a unique start-up with huge potential for growth. |
| How to apply | The application process takes place in collaboration with headhunter and the recruitment agency Profilpartners. Please send your application via Profilpartners' website using the following direct link: www.profilpartners.dk/ledige-stillinger/vis-stilling/?StillingsID=1148 |